Applicants: Donald F. Hooper et al.

Serial No.: 10/713,332

Filed: November 13, 2003

Page : 2 of 15

Attorney's Docket No.: INTEL-006PUS

Intel docket #: P17384

## **AMENDMENTS TO THE SPECIFICATION:**

Please replace the paragraph beginning at page 46, line 21 with the following amended paragraph:

Suitable processors include, by way of example, both general and special purpose microprocessors. Generally, the processor 362 will receive instructions and data from a read-only memory (ROM) 364 and/or a random access memory (RAM) 366 through a CPU bus 368. A computer can generally also receive programs and data from a storage medium such as an internal disk 370 operating through a mass storage interface 372 or a removable disk 374 operating through an I/O interface 376. The flow of data over an I/O bus 378 to and from devices 370, 374, (as well as input device 380, and output device 382 282) and the processor 362 and memory 366, 364 is controlled by an I/O controller 384. User input is obtained through the input device 380 280, which can be a keyboard, mouse, stylus, microphone, trackball, touch-sensitive screen, or other input device. These elements will be found in a conventional desktop computer as well as other computers suitable for executing computer programs implementing the methods described here, which may be used in conjunction with output device 382, which can be any display device (as shown), or other raster output device capable of producing color or gray scale pixels on paper, film, display screen, or other output medium.